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***IN THE UNITED STATES PATENT AND TRADEMARK OFFICE***

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In re application of: Rambaldi et al.

**PATENT**  
Attorney Docket No.: SGSTP009D1

Application No.: Not yet assigned

Examiner: Vu, Ngoc Yen T

Filed: July 3, 2003

Group: 2612

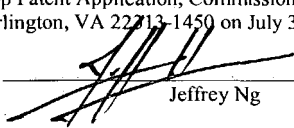
Title: PIXEL CORRECTION SYSTEM AND  
METHOD FOR CMOS IMAGERS

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CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as Express Mail No. EV332828176US to: Mail Stop Patent Application, Commissioner for Patents, P.O. Box 1450, Arlington, VA 22313-1450 on July 3, 2003.

Signed: \_\_\_\_\_

  
Jeffrey Ng

**PRELIMINARY AMENDMENT**

Mail Stop Patent Application  
Commissioner for Patents  
P.O. Box 1450  
Arlington, VA 22313-1450

**Introductory Comments:**

Please consider the following amendments and remarks filed with the Divisional Application mailed on July 3, 2003. The Divisional Application is being filed off of allowed U.S. Patent Application No. 08/964,763 filed on November 5, 1997.